

FREQUENCY SETTING BOARD FIX-2.5G

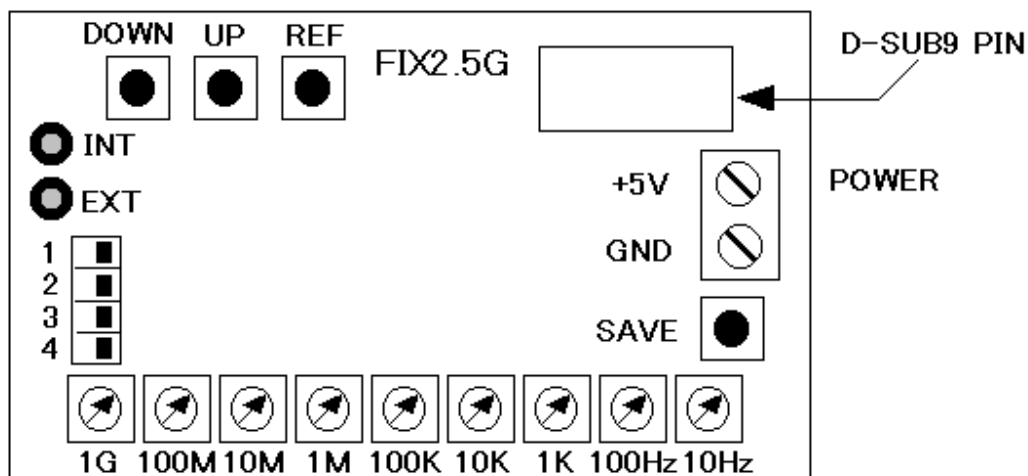
Outline

FIX-2.5G is an option board for DPL-2.5G to be able to set the frequency easily. The desired frequency can be obtained by 9 BCD switches on the board. Also selection of INT/EXT 10MHz reference signal and adjustment of internal reference frequency can be done by this board. A frequency and setting value can be memorized in EEPROM of DPL-2.5G by a save switch on the board.

Connection of DPL-2.5G and FIX-2.5G

DPL-2.5G and FIX-2.5G are connected with the attached D-Sub 9 pin straight cable. Connect +5V DC power supply to CN2.

Layout



Operation

When the power of FIX-2.5G is turned on, FIX-2.5G begins to communicate with DPL-2.5G. All DIP switches on the board should be turned off. Either INT lamp or EXT lamp blinks when the communication is normally done. INT and EXT lamp blink alternately when the communication is failed. Check the connection of the cable and the power supply. After setting the frequency, push the save switch, and the desired frequency will be memorized in EEPROM of DPL-2.5G.

Selection of reference signal

The reference signal of DPL-2.5G can be switched to internal clock or external clock. Either INT or EXT lamp shows the present reference signal. The reference signal is changed by the reference switch and is memorized in EEPROM of DPL-2.5G. Moreover, even if DPL-2.5G is used for itself, the state of the clock is kept in EEPROM.

Adjustment of internal reference signal

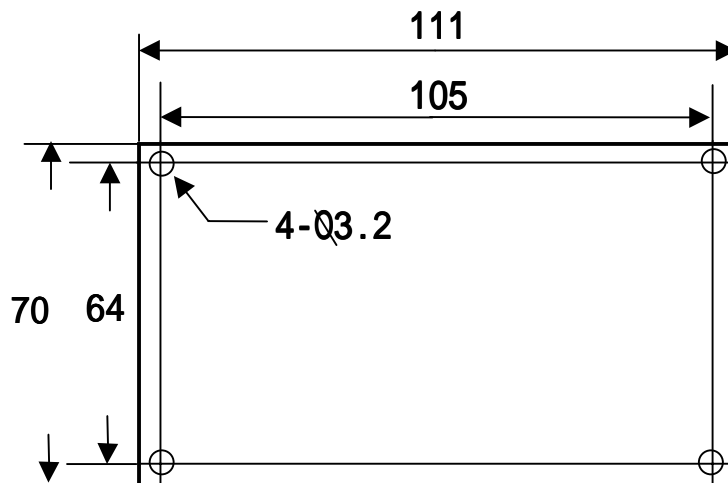
When DIP SW and bit 1 are turned on, internal clock of DPL-2.5G is in adjustment mode and LED lamp blinks slowly and alternately. If the reference clock is in external 10MHz clock, it is not possible to adjust. In this case, as LED lamp blinks first, turn off bit 1 to change the reference signal to internal clock and enter to the adjustment mode again. UP/DOWN switch becomes effective and the desired frequency can be set in the adjustment mode. When adjusting, use an accurate frequency counter. When DIP SW and bit 1 are turned off, the adjustment mode is finished and internal clock frequency is adjusted and memorized in EEPROM of DPL-2.5G.

Specification

Voltage +5V \pm 10%

Current less than 50mA

Dimensions 70x111mm



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