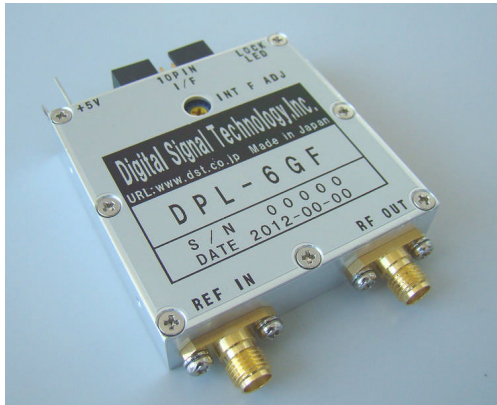
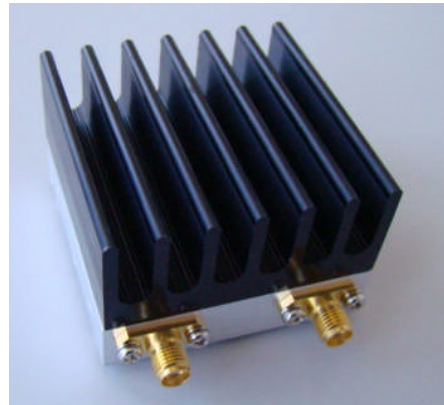


# Programmable Frequency Synthesizer

DPL-6GF



DPL-6GFH(with Heat Sink)



## Features

- Low phase noise
- Programmable(1KHz or 2KHz step)
- 50MHz~6000MHz (Band width : 20%)
- +5V single power supply
- Compact size



Digital Signal Technology, Inc

1-7-30, Higashi Benzai, Asaka, Saitama, 351-0022, Japan

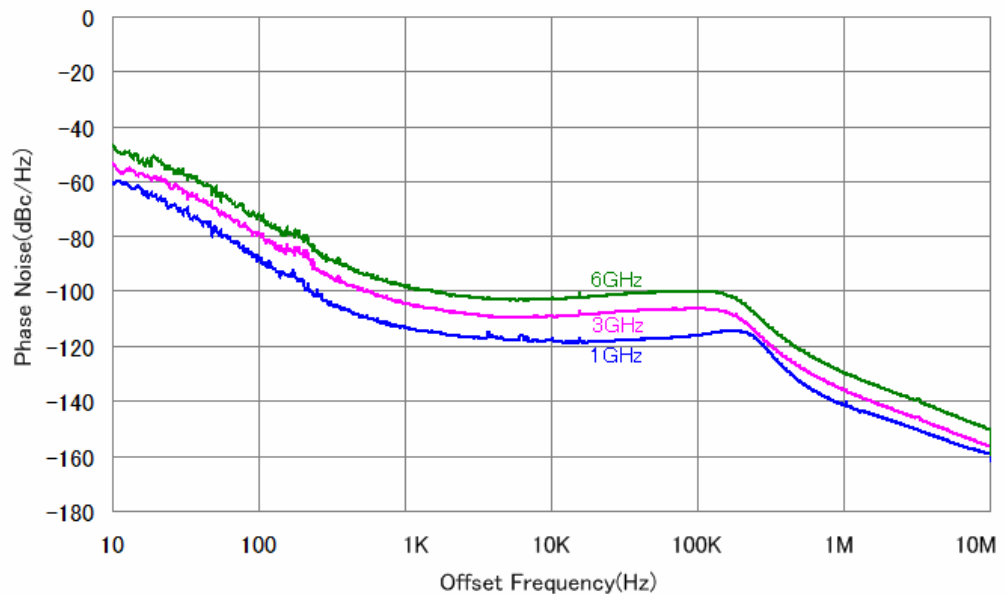
TEL 81-48-468-6094 FAX 81-48-468-6210

<http://www.dst.co.jp/en>

## ● Specification

Power Supply/Current	+5V+/-5%, <600mA	
Frequency Range	50MHz- 6GHz (band width 20%) ** When no output filter is selected as an option, a programmable output in a 1KHz step is allowed from 50MHz to 3GHz(No longer a sine wave).	
Frequency Resolution	50MHz ~ 3GHz 1KHz step	3GHz ~ 6GHz 2KHz step
Output Level	>+10dBm	
Output impedance	50Ω	
Spurious	<-65dBc	
Harmonics	<-40dBc (in case of no output filter option: <-8dBc)	
Phase Noise 6GHz (typical)	-80dBc/Hz	@100Hz
	-97dBc/Hz	@1KHz
	-102dBc/Hz	@10KHz
	-99dBc/Hz	@100KHz
	-126dBc/Hz	@1MHz
Internal Reference Clock Accuracy	<+/-15ppm 0-50 degree C	
External Reference Clock and level	10MHz -6dBm - +6dBm	
Lock Time	Max40msec	
Operating Temperature Range	0 - +50 degree C (In case of being installed with thermal resistance 6.5(degree C/W) heat sink)	
Outer Dimensions	50mm × 50mm × 12.5mm 50mm × 50mm × 33.5mm (with Heat Sink)	
Interface	(1) Asynchronous Serial Communication	9600bps, 8bits, one step bit, non-parity 3.3V CMOS level
	(2) SPI serial communication 3 bytes data	3.3V CMOS level

## ● Phase Noise



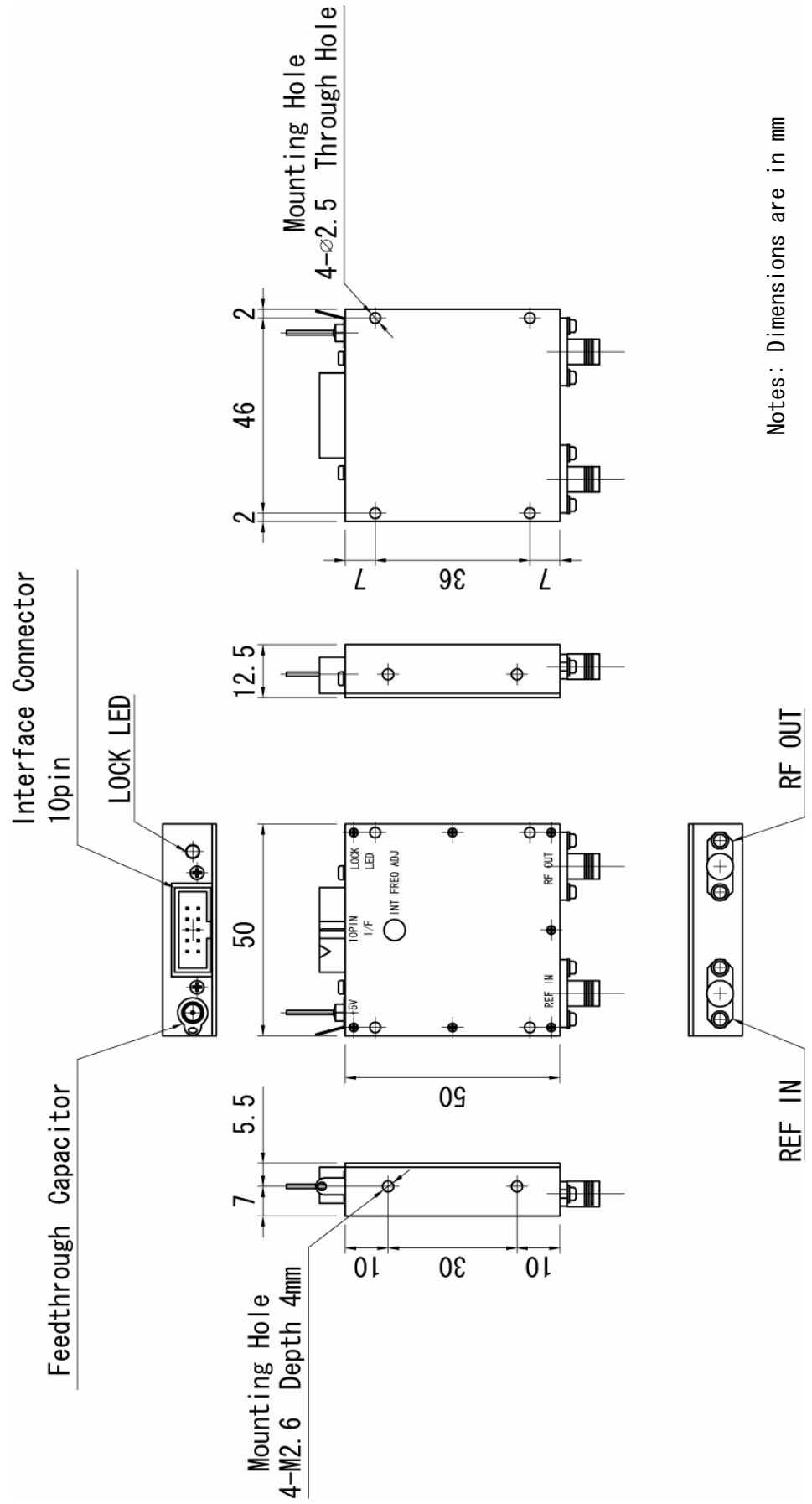
## ● Option

USB interface

USBIF-01

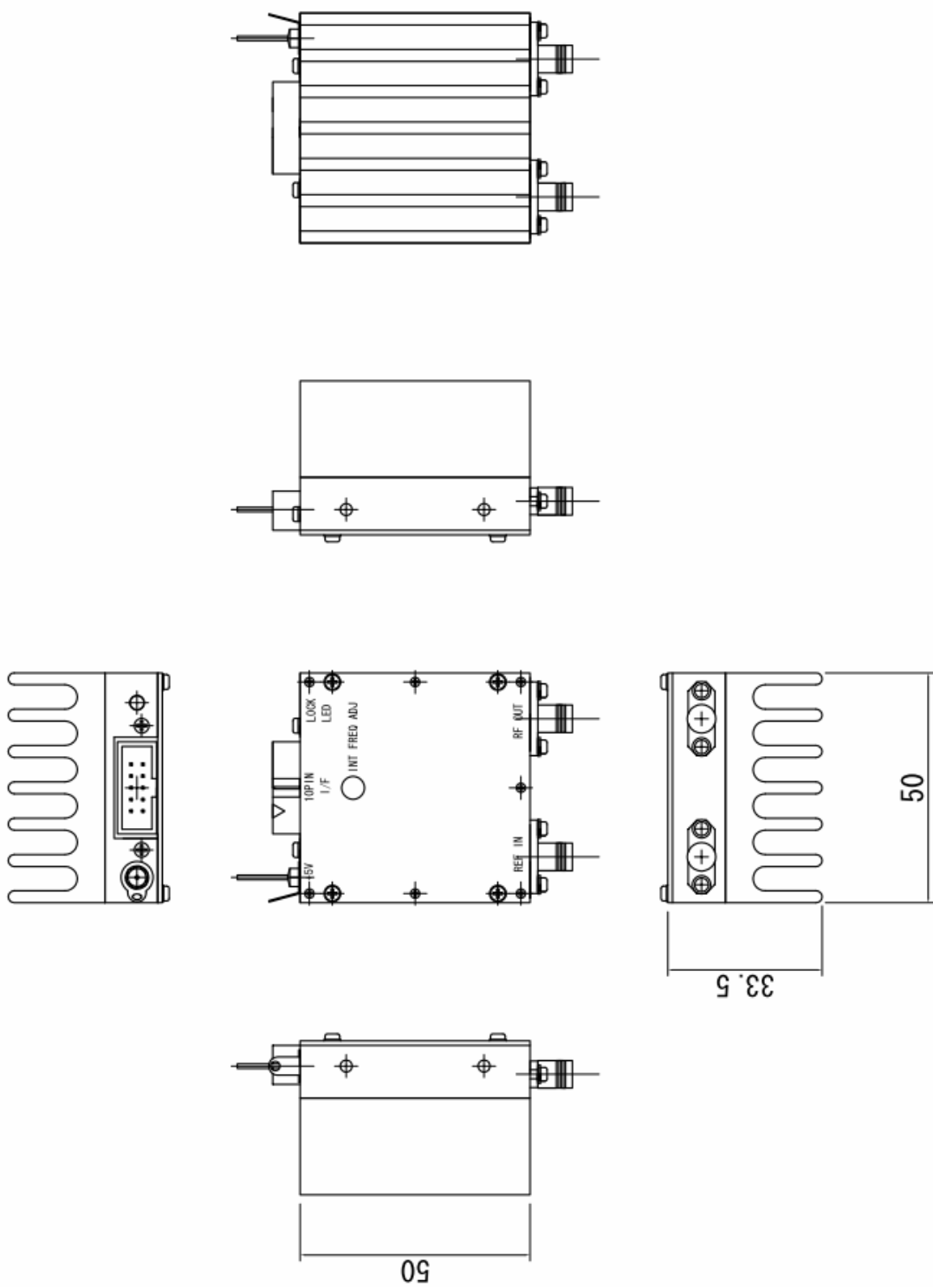


Outer Dimensions  
for DPL-6GF



Notes: Dimensions are in mm

● Outer Dimensions for DPL-6GFH



Notes: Dimensions are in mm