

Fixed Frequency Synthesizer

EPLO



Features

- Sine Wave
- Various output option(LVDS, CMOS and Sine wave 90 degree phase difference two outputs)
- 10MHz-4400MHz(Fixed Frequency)
- Less +10dBm or Over +10dBm output level option
- +5V single power supply
- Internal or External reference clock



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● Specification

Output wave	Sine wave (standard)
Output impedance	50 Ω
Output frequency range	10MHz~4400MHz
Output level	+10dBm+/-2dB (standard)
Spurious level	-60dBc
Harmonic spurious	-30dBc
Internal clock frequency accuracy	+/-2.5ppm (10MHz)
Operating temperature range	0 - +50°C
Power supply/ current	+5V+/-5%, 50mA-180mA(standard) Less than 300mA(in case high output level option)
Dimensions	50 x 13 x 75 (mm) WDH

● Option

External clock input	10MHz - 100MHz (*When this option is required, Internal Clock cannot be used.)
External clock input impedance	High impedance(standard) or 50 Ω possible
Output option	5V-TTL(10M-200M) or 3.3V-TTL(10M-200M) or LVDS(10M-1500M) *
Output level	-10dBm ~ >+10dBm *
High output level	<+10dBm ~ +20dBm *
Sine wave 90 degree difference two outputs	(*When this option is required, High output level option cannot be selected.)

* Only one option should be selected.

● Phase Noise (typical)

Measurement condition:
Sine wave output with internal
reference clock



